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ATTORNEY DOCKET NO. 114596-28-000053

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,065,633 Issue Date: June 20, 2006
Serial No.: 09/626,325 Filed: Jul. 26, 2000
Applicant: John S. Yates, Jr. et al.
Title: OPERATING SYSTEM FOR COMPUTER WITH TWO ARCHITECTURES
Atty. Docket: 114596-28-000053 Art Unit: unassigned

CERTIFICATE OF MAILING (37 C.F.R. § 1.8a)

Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
JUL 07 2006
of Correction

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- Request for Certificate of Correction
- Form PTO/SB/44

(along with any paper(s) referred to as being attached or enclosed) are being deposited with the United States Postal Service on the date shown below with sufficient postage as first-class mail in an envelope addressed to Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Respectfully submitted,

WILLKIE FARR & GALLAGHER LLP

Dated: June 29, 2006

By: 

David E. Boundy
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PATENT



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REQUEST FOR CERTIFICATE OF CORRECTION

Certificate of Correction Branch
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P.O. Box 1450
Alexandria, VA 22313-1450

REMARKS

A Certificate of Correction is requested as set forth on the attached form(s) PTO/SB/44.

All mistakes in the patent sought to be corrected were incurred thought the fault of the Patent and Trademark Office, and are clearly disclosed in the records of the Office. Accordingly, correction is requested under 35 U.S.C. § 254 and 37 C.F.R. § 1.322, and no fee is required.

In accordance with the policy set forth in the notice appearing in 824 PTO Off. Gaz. 1199, concerning corrections pursuant to Rule 323, it is requested that the following corrections be made of record in the file of the above-identified U.S. patent:

At col. 93, line 47, change "%" to -- x --

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on June 29, 2006 as First Class Mail in an envelope with sufficient postage addressed to Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

JUL 07 2006

Prompt issuance of the Certificate of Correction is respectfully requested.

Kindly charge any additional fee, or credit any surplus, to Deposit Account No. 23-2405,
Order No. 114596-28-000053.

Respectfully submitted,

WILLKIE FARR & GALLAGHER LLP

Dated: June 29, 2006

By: 

David E. Boundy

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 7,065,633
APPLICATION NO. : 09/626,325
DATED : June 20, 2006
INVENTOR(S) : John S. Yates, Jr. et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

The title is -- OPERATING SYSTEM FOR COMPUTER WITH TWO ARCHITECTURES --.

At col. 54, line 35, "arrow 1" should be --arrow ①--.

At col. 54, line 41, "arrow 2" should be --arrow ②--.

At col. 54, line 49, "arrow 3" should be --arrow ③--.

At col. 54, line 61, "arrow 4" should be --arrow ④--.

At col. 54, line 64, "arrow 5" should be --arrow ⑤--.

At col. 55, line 3, "arrow 6" should be --arrow ⑥--.

At col. 55, line 11, "arrow 7" should be --arrow ⑦--.

At col. 55, line 20, "arrow 8" should be --arrow ⑧--.

At col. 55, line 21, "arrow 9" should be --arrow ⑨--.

At col. 55, line 29, "arrow 10" should be --arrow ⑩--.

At col. 55, line 31, "arrow 11" should be --arrow ⑪--.

At col. 55, line 41, "arrow 12" should be --arrow ⑫--.

At col. 55, line 44, "arrow 13" should be --arrow ⑬--.

At col. 55, line 55, "arrow 14" should be --arrow ⑭--.

At col. 56, line 3, "arrow 15" should be --arrow ⑮--.

At col. 56, line 15, "arrow 16" should be --arrow ⑯--.

At col. 56, line 18, "arrow 17" should be --arrow ⑰--.

At col. 56, line 20, "arrow 18" should be --arrow ⑱--.

At col. 56, line 24, "arrow 19" should be --arrow ⑲--.

At col. 56, line 32, "arrow 20" should be --arrow ⑳--.

At col. 56, line 42, "arrow 21" should be --arrow ㉑--.

At col. 56, line 44, "arrow 22" should be --arrow ㉒--.

At col. 56, line 47, "arrow 23" should be --arrow ㉓--.

At col. 63, line 26, "FP" should be -- F0 --.

At col. 89, line 11, "probable" should be -- probeable --.

At col. 94, line 56, "Sa" should be --5a--.

At col. 100, line 49, "7430R's" should be --743 OR's--.

At col. 101, line 27 "32=2 its" should be -- 32 = 2⁵ bits --

At col. 127, line 4, "9c" should be deleted.

In claim 13, col. 149, line 42, "an execution" should be -- execution --.